

IN THE SPECIFICATION:

Page 5, Line 1, after the word "but," please insert the word --is--.

Page 8, line 26, please change "experiences" to --experience--.

Page 11, line 21, please delete --in--.

IN THE CLAIMS:

Please cancel claim 19 without prejudice or disclaimer of subject matter.

Please amend the claims as follows:

1. (Amended) A circuit comprising:

a first circuit having a first input and a first output, wherein said first output [being] includes a function of a signal at said first input [plus a] and also includes a first noise component resulting from noise experienced by said first circuit;

a second circuit, [identical to and] located [proximate] proximal to said first circuit[,] and having a second input and a second output, wherein said second output includes a function of a signal at said second input and also includes a second noise component resulting from noise experienced by said second circuit, and wherein the second noise component is approximately equal to the first noise component;

Sub
E1
B1

B

*B1
Cancelled*

[said second input set to cause said second output to equal said noise component of said first output;] and

a subtractor circuit connected to said first circuit and to said second circuit to subtract said second output from said first output.

*Sub
C1*

3. (Amended) A circuit according to claim 1 wherein said subtractor circuit further comprises a halving circuit which reduces a signal by one-half its amplitude.

B2

4. (Amended) A circuit comprising:

a first circuit having a first input and a first output, [said first output being a function of said first input plus noise;] wherein said first output includes a function of a signal at said first input and also includes a first noise component resulting from noise experienced by said first circuit;

*Sub
E3*

[a second circuit, identical to said first circuit, and] a second circuit having a second input and a second output, wherein said second output includes an input signal component which is a function of a signal at said second input and also includes a second noise component resulting from noise experienced by said second circuit, wherein the input signal component is a null output, and wherein the second noise component is approximately equal to the first noise component;

B2
concl

[said second input designed to cause said second circuit to produce, as said second output, said noise only;] and

a third circuit having a third input connected to said first output[,] and a fourth input connected to said second output to subtract said second output from said first output.

Sub
C2
B3

7. (Amended) A circuit comprising:

a first circuit having a first input and a first output, wherein said first output [being] includes a function of a signal at said first input and also includes a noise component resulting from noise experienced by [plus noise;

a second circuit, identical to] said first circuit[, and];

a second circuit having a second input and a second output;

[said second input, being an inverse of said first input, causing said second circuit to produce, as said second output, an inverse function of said first circuit plus noise] a signal supplying circuit supplying to the second input a signal an inverse of the signal at the first input; and

a third circuit having a third input connected to said first output and a fourth input connected to said second output, and [combining] subtracting said second output from said first output.

8. (Amended) A circuit according to claim 7 wherein said third circuit further comprises a halving circuit which reduces a signal by one-half its amplitude.

9. (Amended) A circuit according to claim 8, wherein said first circuit, said second circuit, said [operator] third circuit, and said digital circuit are on a single integrated circuit chip.

11. (Twice Amended) An integrated circuit chip (IC) [circuit] comprising:
[a digital circuit;]
a plurality of analog circuits, each proximal to [said digital circuit and] to each other, and each of said plurality of analog circuits producing an output signal which includes a function of an input signal and also includes a noise component resulting from noise experienced by said plurality of analog circuits;
a noise separator circuit, proximal to said plurality of analog circuits, and producing a noise signal based on noise experienced by said noise separator circuit, wherein the noise signal is approximately equal to the noise component of the output signal output by each of the plurality of analog circuits; and
a noise canceling circuit[, processing said outputs signal] which processes said output signals with said noise signal to reduce the noise component of the output signal output by each of the plurality of analog circuits.

B6 Sub C3
13. (Amended) An IC according to claim 11 wherein said noise canceling circuit further comprises a halving circuit which reduces a signal by one-half its amplitude.

Sub E3
14. (Amended) A noise cancellation method comprising the steps:
supplying a first signal to a first circuit;
reading a first output from said first circuit;
supplying a [null] signal to a second circuit which results in a null output from the second circuit, wherein said second circuit is located proximal to said first circuit;
reading a second output from said second circuit;
combining said first output with said second output to produce a combinational output,
wherein a noise component of the first output due to noise experienced by said first circuit is approximately equal to a noise component of the second circuit due to noise experienced by said second circuit.

B7
17. (Amended) A method according to claim 14 wherein said step of combination comprises the step of adding said second output to said first output to produce an added output.

B7
B7C
B7D
B7E
B7F
B7G
B7H
B7I
B7J
B7K
B7L
B7M
B7N
B7O
B7P
B7Q
B7R
B7S
B7T
B7U
B7V
B7W
B7X
B7Y
B7Z
B7AA
B7AB
B7AC
B7AD
B7AE
B7AF
B7AG
B7AH
B7AI
B7AJ
B7AK
B7AL
B7AM
B7AN
B7AO
B7AP
B7AQ
B7AR
B7AS
B7AT
B7AU
B7AV
B7AW
B7AX
B7AY
B7AZ
B7BA
B7BB
B7BC
B7BD
B7BE
B7BF
B7BG
B7BH
B7BI
B7BJ
B7BK
B7BL
B7BM
B7BN
B7BO
B7BP
B7BQ
B7BR
B7BS
B7BT
B7BU
B7BV
B7BW
B7BX
B7BY
B7BZ
B7CA
B7CB
B7CC
B7CD
B7CE
B7CF
B7CG
B7CH
B7CI
B7CJ
B7CK
B7CL
B7CM
B7CN
B7CO
B7CP
B7CQ
B7CR
B7CS
B7CT
B7CU
B7CV
B7CW
B7CX
B7CY
B7CZ
B7DA
B7DB
B7DC
B7DD
B7DE
B7DF
B7DG
B7DH
B7DI
B7DJ
B7DK
B7DL
B7DM
B7DN
B7DO
B7DP
B7DQ
B7DR
B7DS
B7DT
B7DU
B7DV
B7DW
B7DX
B7DY
B7DZ
B7EA
B7EB
B7EC
B7ED
B7EE
B7EF
B7EG
B7EH
B7EI
B7EJ
B7EK
B7EL
B7EM
B7EN
B7EO
B7EP
B7EQ
B7ER
B7ES
B7ET
B7EU
B7EV
B7EW
B7EX
B7EY
B7EZ
B7FA
B7FB
B7FC
B7FD
B7FE
B7FF
B7FG
B7FH
B7FI
B7FJ
B7FK
B7FL
B7FM
B7FN
B7FO
B7FP
B7FQ
B7FR
B7FS
B7FT
B7FU
B7FV
B7FW
B7FX
B7FY
B7FZ
B7GA
B7GB
B7GC
B7GD
B7GE
B7GF
B7GG
B7GH
B7GI
B7GJ
B7GK
B7GL
B7GM
B7GN
B7GO
B7GP
B7GQ
B7GR
B7GS
B7GT
B7GU
B7GV
B7GW
B7GX
B7GY
B7GZ
B7HA
B7HB
B7HC
B7HD
B7HE
B7HF
B7HG
B7HH
B7HI
B7HJ
B7HK
B7HL
B7HM
B7HN
B7HO
B7HP
B7HQ
B7HR
B7HS
B7HT
B7HU
B7HV
B7HW
B7HX
B7HY
B7HZ
B7IA
B7IB
B7IC
B7ID
B7IE
B7IF
B7IG
B7IH
B7II
B7IJ
B7IK
B7IL
B7IM
B7IN
B7IO
B7IP
B7IQ
B7IR
B7IS
B7IT
B7IU
B7IV
B7IW
B7IX
B7IY
B7IZ
B7JA
B7JB
B7JC
B7JD
B7JE
B7JF
B7JG
B7JH
B7JI
B7JJ
B7JK
B7JL
B7JM
B7JN
B7JO
B7JP
B7JQ
B7JR
B7JS
B7JT
B7JU
B7JV
B7JW
B7JX
B7JY
B7JZ
B7KA
B7KB
B7KC
B7KD
B7KE
B7KF
B7KG
B7KH
B7KI
B7KJ
B7KK
B7KL
B7KM
B7KN
B7KO
B7KP
B7KQ
B7KR
B7KS
B7KT
B7KU
B7KV
B7KW
B7KX
B7KY
B7KZ
B7LA
B7LB
B7LC
B7LD
B7LE
B7LF
B7LG
B7LH
B7LI
B7LJ
B7LK
B7LL
B7LM
B7LN
B7LO
B7LP
B7LQ
B7LR
B7LS
B7LT
B7LU
B7LV
B7LW
B7LX
B7LY
B7LZ
B7MA
B7MB
B7MC
B7MD
B7ME
B7MF
B7MG
B7MH
B7MI
B7MJ
B7MK
B7ML
B7MM
B7MN
B7MO
B7MP
B7MQ
B7MR
B7MS
B7MT
B7MU
B7MV
B7MW
B7MX
B7MY
B7MZ
B7NA
B7NB
B7NC
B7ND
B7NE
B7NF
B7NG
B7NH
B7NI
B7NJ
B7NK
B7NL
B7NM
B7NN
B7NO
B7NP
B7NQ
B7NR
B7NS
B7NT
B7NU
B7NV
B7NW
B7NX
B7NY
B7NZ
B7OA
B7OB
B7OC
B7OD
B7OE
B7OF
B7OG
B7OH
B7OI
B7OJ
B7OK
B7OL
B7OM
B7ON
B7OO
B7OP
B7OQ
B7OR
B7OS
B7OT
B7OU
B7OV
B7OW
B7OX
B7OY
B7OZ
B7PA
B7PB
B7PC
B7PD
B7PE
B7PF
B7PG
B7PH
B7PI
B7PJ
B7PK
B7PL
B7PM
B7PN
B7PO
B7PP
B7PQ
B7PR
B7PS
B7PT
B7PU
B7PV
B7PW
B7PX
B7PY
B7PZ
B7QA
B7QB
B7QC
B7QD
B7QE
B7QF
B7QG
B7QH
B7QI
B7QJ
B7QK
B7QL
B7QM
B7QN
B7QO
B7QP
B7QQ
B7QR
B7QS
B7QT
B7QU
B7QV
B7QW
B7QX
B7QY
B7QZ
B7RA
B7RB
B7RC
B7RD
B7RE
B7RF
B7RG
B7RH
B7RI
B7RJ
B7RK
B7RL
B7RM
B7RN
B7RO
B7RP
B7RQ
B7RR
B7RS
B7RT
B7RU
B7RV
B7RW
B7RX
B7RY
B7RZ
B7SA
B7SB
B7SC
B7SD
B7SE
B7SF
B7SG
B7SH
B7SI
B7SJ
B7SK
B7SL
B7SM
B7SN
B7SO
B7SP
B7SQ
B7SR
B7SS
B7ST
B7SU
B7SV
B7SW
B7SX
B7SY
B7SZ
B7TA
B7TB
B7TC
B7TD
B7TE
B7TF
B7TG
B7TH
B7TI
B7TJ
B7TK
B7TL
B7TM
B7TN
B7TO
B7TP
B7TQ
B7TR
B7TS
B7TT
B7TU
B7TV
B7TW
B7TX
B7TY
B7TZ
B7UA
B7UB
B7UC
B7UD
B7UE
B7UF
B7UG
B7UH
B7UI
B7UJ
B7UK
B7UL
B7UM
B7UN
B7UO
B7UP
B7UQ
B7UR
B7US
B7UT
B7UU
B7UV
B7UW
B7UX
B7UY
B7UZ
B7VA
B7VB
B7VC
B7VD
B7VE
B7VF
B7VG
B7VH
B7VI
B7VJ
B7VK
B7VL
B7VM
B7VN
B7VO
B7VP
B7VQ
B7VR
B7VS
B7VT
B7VU
B7VV
B7VW
B7VX
B7VY
B7VZ
B7WA
B7WB
B7WC
B7WD
B7WE
B7WF
B7WG
B7WH
B7WI
B7WJ
B7WK
B7WL
B7WM
B7WN
B7WO
B7WP
B7WQ
B7WR
B7WS
B7WT
B7WU
B7WV
B7WW
B7WX
B7WY
B7WZ
B7XA
B7XB
B7XC
B7XD
B7XE
B7XF
B7XG
B7XH
B7XI
B7XJ
B7XK
B7XL
B7XM
B7XN
B7XO
B7XP
B7XQ
B7XR
B7XS
B7XT
B7XU
B7XV
B7XW
B7XX
B7XY
B7XZ
B7YA
B7YB
B7YC
B7YD
B7YE
B7YF
B7YG
B7YH
B7YI
B7YJ
B7YK
B7YL
B7YM
B7YN
B7YO
B7YP
B7YQ
B7YR
B7YS
B7YT
B7YU
B7YV
B7YW
B7YX
B7YY
B7YZ
B7ZA
B7ZB
B7ZC
B7ZD
B7ZE
B7ZF
B7ZG
B7ZH
B7ZI
B7ZJ
B7ZK
B7ZL
B7ZM
B7ZN
B7ZO
B7ZP
B7ZQ
B7ZR
B7ZS
B7ZT
B7ZU
B7ZV
B7ZW
B7ZX
B7ZY
B7ZZ

18. (Amended) A method according to claim 17 wherein said step of combination further comprises the step of [halving] reducing said added output signal by one-half its amplitude.

Please add claims 20 to 29 as follows:

20. A circuit according to claim 1, wherein said second circuit is identical to said first circuit.

21. A circuit according to claim 1, wherein the noise experienced by said first circuit and said second circuit is electromagnetic environmental noise.

22. A circuit according to claim 1 wherein said second circuit is located close enough to said first circuit so that said second circuit experiences approximately the same noise as said first circuit.

23. A circuit according to claim 1, wherein said ~~third~~ ^{subtractor} circuit is digital.

24. A circuit according to claim 1, wherein said ~~third~~ ^{subtractor} circuit is analog.

WIFI
~~10~~
~~25.~~ A circuit according to claim ~~4~~⁸, wherein said second circuit is identical to said first circuit.

~~11~~
~~26.~~ A circuit according to claim ~~4~~⁸, wherein the noise experienced by said first circuit and said second circuit is electromagnetic environmental noise.

B81 added
~~16~~
~~27.~~ A circuit according to claim ~~7~~¹², wherein said second circuit is identical to said first circuit.

~~20~~
~~28.~~ A circuit according to claim ~~11~~¹⁷, wherein the noise experienced by said plurality of analog circuits and said noise separator circuit is electromagnetic environmental noise.

~~24~~
~~29.~~ A circuit according to claim ~~14~~²¹, wherein the noise experienced by said first circuit and said second circuit is electromagnetic environmental noise.--

IN THE DRAWINGS:

Please approve the accompanying Request for Approval of Drawing Changes.